



UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS
UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231
www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 5909

SERIAL NUMBER 09/841,536	FILING DATE 04/24/2001 RULE	CLASS 712	GROUP ART UNIT 2183	ATTORNEY DOCKET NO.
APPLICANTS Sanjay Agarwal, Santa Clara, CA; Sapna Agrawal, Santa Clara, CA;				
** CONTINUING DATA *****				
** FOREIGN APPLICATIONS *****				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 06/18/2001				
** SMALL ENTITY **				
Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged <i>[Signature]</i> Examiner's Signature Initials		STATE OR COUNTRY CA	SHEETS DRAWING 14	TOTAL CLAIMS 3
				INDEPENDENT CLAIMS 3
ADDRESS Sanjay Agarwal CHIPSOL, Inc. 4702 Cheeney St. Santa Clara, CA 95054				
TITLE Coprocessor architecture for control processors using synchronous logic				
FILING FEE RECEIVED 355	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	